

Selectable-Range Current Loop

This article shows an example of implementing a 4-20mA or 0-20mA current-loop output using a voltage output digital-to-analog converter (DAC) and a V to I converter.

Current-loop signals, as opposed to voltage signals, are commonly used in industrial systems because they are much less subject to noise and relatively indifferent to line length (because the current is unaffected by line resistance). The circuit of Figure 1 allows digital selection between the two standard ranges of current-loop transmission common in industrial systems (0-20mA and 4-20mA).

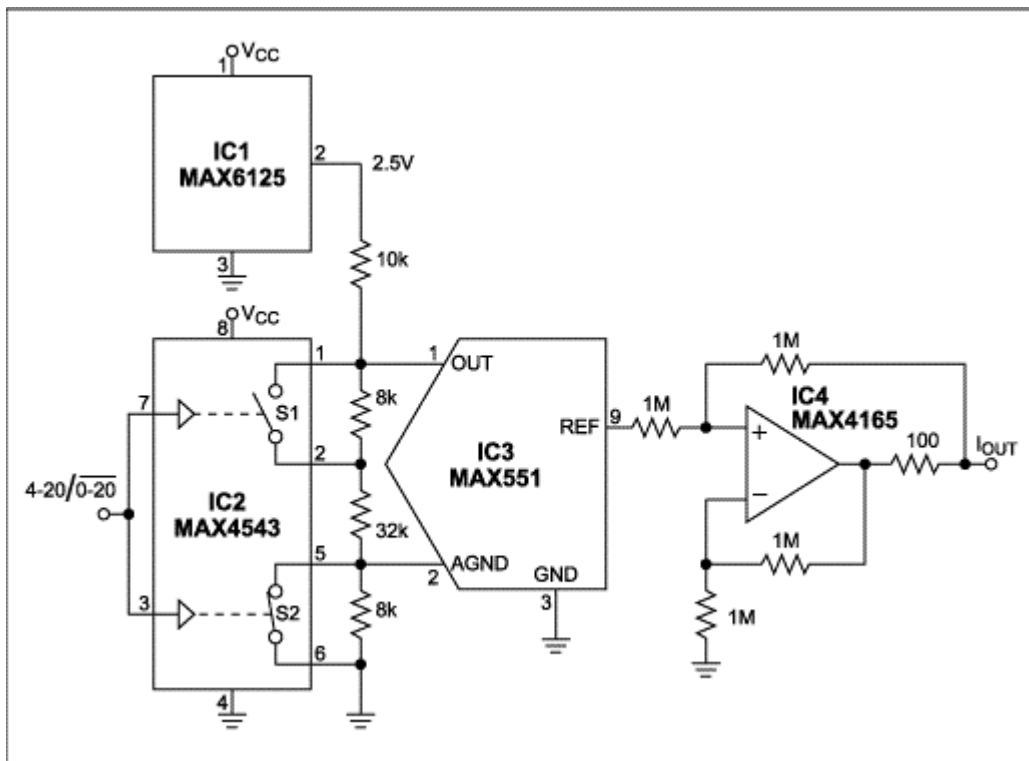


Figure 1. This circuit's single control input selects a current-loop range of 0-20mA or 4-20mA.

The analog switch (IC2) controls a resistive divider driven by the 2.5V reference (IC1), such that the AGND offset is either 0V or 0.4V, and the DAC's Δ voltage (OUT minus AGND) is either 2V or 1.6V. Thus,

for 0mA to 20mA, $\Delta = 2V$ and AGND = 0V;

for 4mA to 20mA, $\Delta = 1.6V$ and AGND = 0.4V.

The voltage-output D/A converter (IC3) drives a V/I converter (op amp IC4 and associated resistors) to produce the current loop. Because the DAC's output resistance in voltage mode is high (11k Ω), the resistor values are made large to preserve accuracy. The op amp's output-drive capability is 80mA. To maintain good linearity, the reference voltage at OUT (IC3 pin 1) should not exceed 2V.

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